

[Abstract] [PDF Full-Text (520 KB)] **IEEE CNF**

2 Phased array shaped multi-beam optimization for LEO satellite communications using a genetic algorithm

Sherman, K.N.; Phased Array Systems and Technology, 2000. Proceedings. 2000 IEEE International Conference on , 21-25 May 2000 Page(s): 501 -504

[Abstract] [PDF Full-Text (308 KB)] **IEEE CNF**

3 A methodology for converting polygon based standard cell from bulk CMOS to SOI

Wu, K.Y.; Chan, P.C.H.; Electron Devices Meeting, 1994. Proceedings., 1994 IEEE Hong Kong, 18 July 1994 Page(s): 8-11

·

[Abstract] [PDF Full-Text (232 KB)] **IEEE CNF**

4 Smallest paths in simple rectilinear polygons

McDonald, K.M.; Peters, J.G.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 11 Issue: 7 , July 1992

Page(s): 864 -875

[Abstract] [PDF Full-Text (1108 KB)] IEEE JNL

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account | New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online Publications | Help | FAQ | Terms | Back to Top

Copyright © 2003 IEEE — All rights reserved